

REMARKS/ARGUMENTS

Favorable reconsideration of this application in light of the following discussion is respectfully requested.

Claims 1-12 are pending in the present application. No claims are amended, canceled or added by the present amendment.

In the outstanding Office Action, Claims 1, 2-4 and 10-12 were rejected under 35 U.S.C. § 103(a) as unpatentable over Applicant's Admitted Art (herein "AA") in view of U.S. Patent No. 6,523,136 to Higashida and U.S. Patent No. 5,416,919 to Ogino et al. (herein "Ogino"); and Claims 5-9 were rejected under 35 U.S.C. § 103(a) as unpatentable over AA in view of Higashida, Ogino and U.S. Patent No. 5,566,303 to Tashiro et al. (herein "Tashiro").

Applicant respectfully traverses the rejection of Claims 1, 2-4 and 10-12 under 35 U.S.C. § 103(a) as unpatentable over AA, Higashida and Ogino.

Claim 1 is directed to a system LSI formed on one chip including, in part, a selection means for optionally selecting one output of a program counter, a computing unit and a register in the processor circuit, at least one output of a storage circuit, and one output of a plurality of internal signals in a peripheral circuit including an output of a functional block. Further, the system LSI also includes a selection control means for controlling selection of a result signal from any operation process in any place of the processor circuit, the storage circuit and the peripheral circuit, based on a selection signal that is supplied from the outside of the system LSI via an external terminal.

In a non-limiting embodiment, Applicant's Figure 4 shows a system LSI having selection means 30 for selecting any one output of all the elements in the system LSI, including the processor circuit 1, storage circuit 2, and peripheral circuit 3. Thus, a system LSI according to Claim 1 advantageously has only one output terminal for a result signal that may be configured to include results from every portion of the system LSI.

Applicant respectfully submit that AA, Higashida and Ogino, whether taken individually or in combination, fail to teach or suggest all the features of the independent claims. The system LSI of the AA shown in Applicant's Figure 3 includes two output terminals for monitor signals 59, including an upper output terminal 59 provided from the PMU core 51 and a lower output terminal 59 provided from the peripheral circuit 56. Thus, Applicant respectfully submits that AA does not teach or suggest "selection means for selecting one output," as recited in Claim 1.

Similarly, Applicant submits that Higashida does not teach or suggest the selection means for selecting any one of the debug operation signals, as well as only one output terminal for monitor or result signals, and Higashida does not disclose only one terminal for outputting only one result signal which is selected by the selection means. Thus, Applicant respectfully submits that Higashida also does not teach or suggest "selection means for selecting one output," as recited in Claim 1.

In addition, Applicant submits that Ogino does not teach or suggest a selection means for optionally selecting one output signal of a plurality of internal function blocks. As noted in the Office Action, Ogino describes a signal control circuit 13 that outputs address bus 14, data bus 15 and control bus 16 signals based on a testmode setting circuit 24. However, Ogino does not indicate or suggest that the signal control circuit optionally selects one output signal. Thus, Applicant respectfully submits that Ogino also fails to teach or suggest "selection means for selecting one output," as recited in Claim 1.

Accordingly, Applicant respectfully submits that independent Claim 1 and claims depending therefrom patentably define over any combination of AA, Higashida and Ogino, and thus respectfully submits that Claim 1 and claims depending therefrom are allowable.

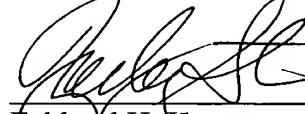
Further, Applicant respectfully traverses the rejection of Claims 5-9 under 35 U.S.C. § 103(a) as unpatentable over AA, Higashida, Ogino and Tashiro.

Claims 5-9 depend from Claim 1, which as discussed above is believed to be allowable. Further, Applicant respectfully submits that Tashiro does not teach or suggest the claimed features lacking in the disclosures of AA, Higashida and Ogino. Accordingly, Applicant respectfully requests that rejection be withdrawn.

Consequently, in light of the above discussion, the present application is believed to be in condition for allowance and an early and favorable action to that effect is respectfully requested.

Respectfully submitted,

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